

P-Ch 150V Fast Switching MOSFETs

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary



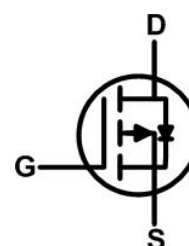
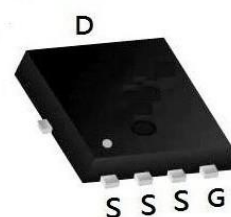
BVDSS	RDSON	ID
-150V	78mΩ	-30A

Description

The UD30P15F is the highest performance trench P-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The UD30P15F meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

PDFN5060-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-150	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	-30	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10V^{1,6}$	-16	A
I_{DM}	Pulsed Drain Current ²	-100	A
EAS	Single Pulse Avalanche Energy ³	380	mJ
I_{AS}	Avalanche Current	---	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation ⁴	23	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	---	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.2	$^\circ\text{C/W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-150	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA	---	---	---	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-12A	---	78	105	mΩ
		V _{GS} =-4.5V, I _D =-8A	---	80	109	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1	-1.8	-2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient		---	---	---	mV/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-150V, V _{GS} =0V, T _J =25°C	---	---	-1	uA
		V _{DS} =-150V, V _{GS} =0V, T _J =100°C	---	---	-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-3	---	11	---	S
R _g	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	0.8	---	Ω
Q _g	Total Gate Charge	V _{DS} =-75V, V _{GS} =-10V, I _D =-3A	---	142	---	nC
Q _{gs}	Gate-Source Charge		---	26.7	---	
Q _{gd}	Gate-Drain Charge		---	50	---	
T _{d(on)}	Turn-On Delay Time	V _{GS} =-10V, V _{DS} =-75V, I _D =-3A, R _{GEN} =3Ω	---	36.2	---	ns
T _r	Rise Time		---	136	---	
T _{d(off)}	Turn-Off Delay Time		---	85	---	
T _f	Fall Time		---	60	---	
C _{iss}	Input Capacitance	V _{DS} =-75V, V _{GS} =0V, f=1MHz	---	8178	---	pF
C _{oss}	Output Capacitance		---	127	---	
C _{rss}	Reverse Transfer Capacitance		---	114	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current ^{1,4}	V _G =V _D =0V, Force Current	---	---	-30	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =-3A, T _J =25°C	---	---	-1.2	V
t _{rr}	Reverse Recovery Time	I _F =-3A, di/dt=100A / μs, T _J =25°C	---	88	---	nS
Q _{rr}	Reverse Recovery Charge		---	182	---	nC

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C
2. The EAS data shows Max. rating . The test condition is V_D=-50V, V_G=-10V, R_G=25ohm, L=0.5mH
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Electrical And Thermal Characteristics (Curves)

Figure 1. Output Characteristics

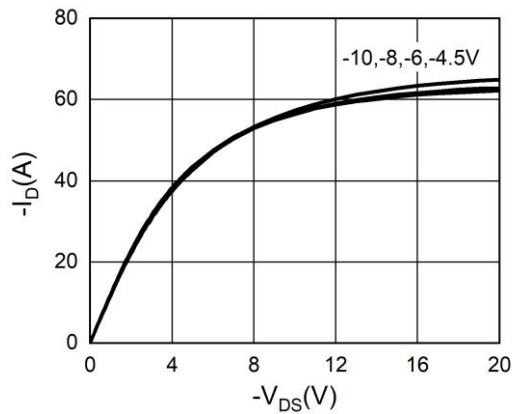


Figure 2. Transfer Characteristics

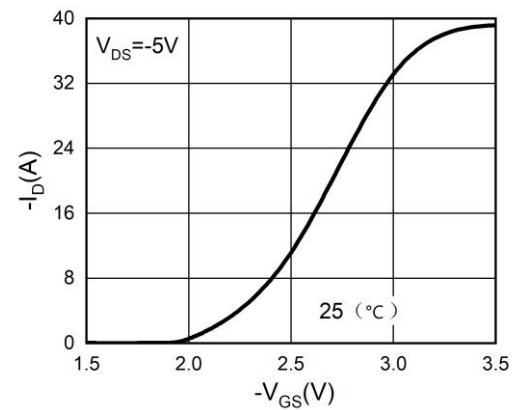


Figure 3. Power Dissipation

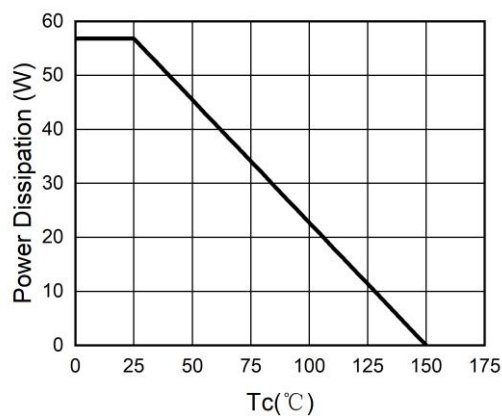


Figure 4. Drain Current

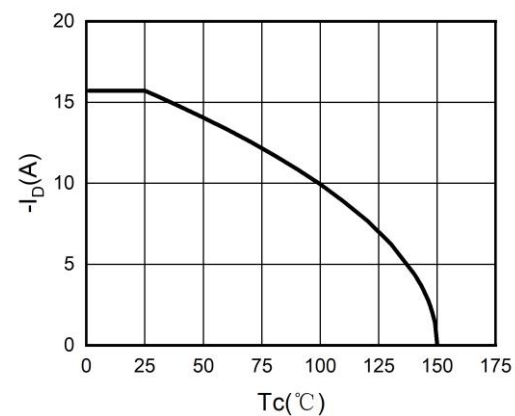


Figure 5. BV_{DSS} vs Junction Temperature

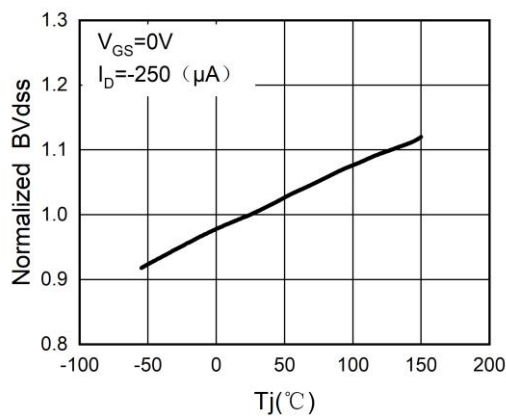
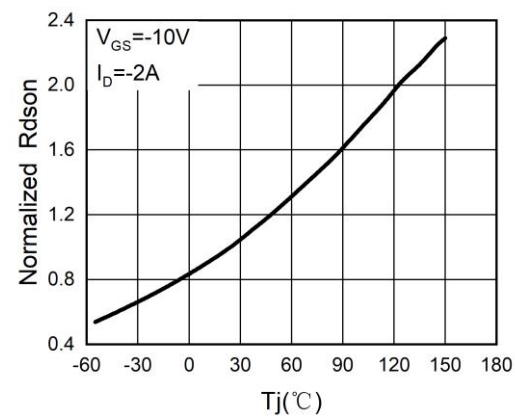


Figure 6. $R_{DS(ON)}$ vs Junction Temperature



Typical Electrical And Thermal Characteristics (Curves)

Figure 7. Gate Charge Waveforms

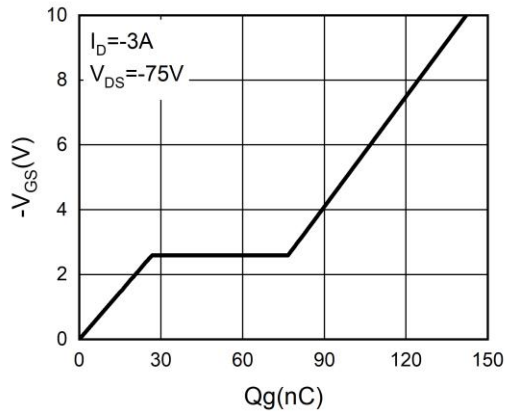


Figure 8. Capacitance

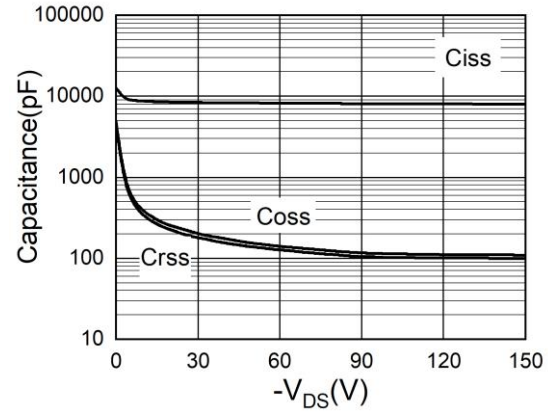


Figure 9. Body-Diode Characteristics

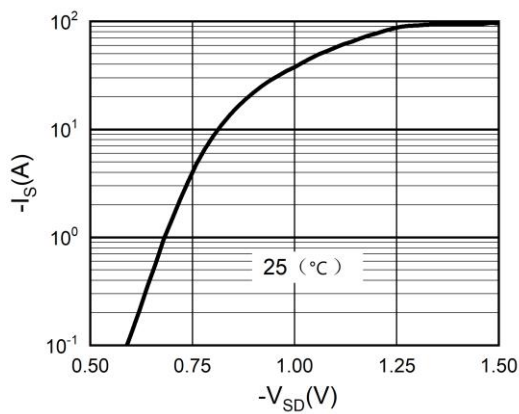
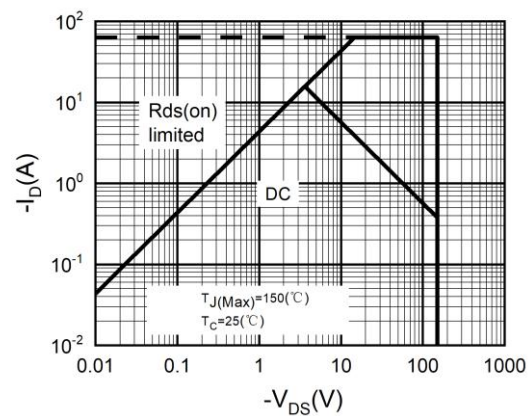
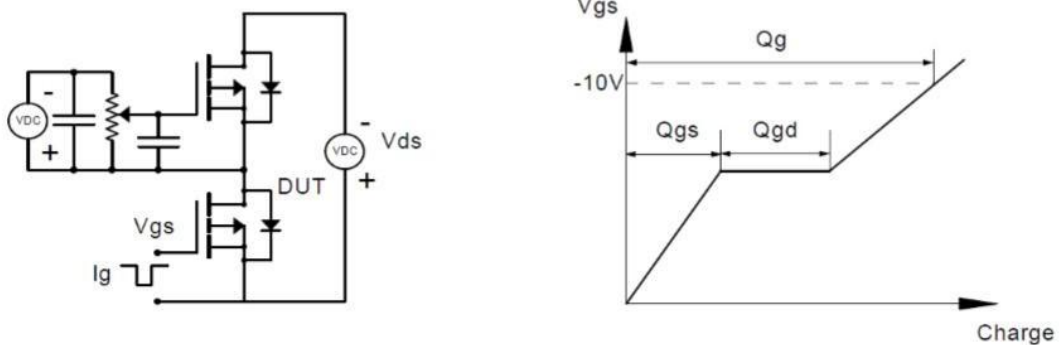


Figure 10. Maximum Safe Operating Area

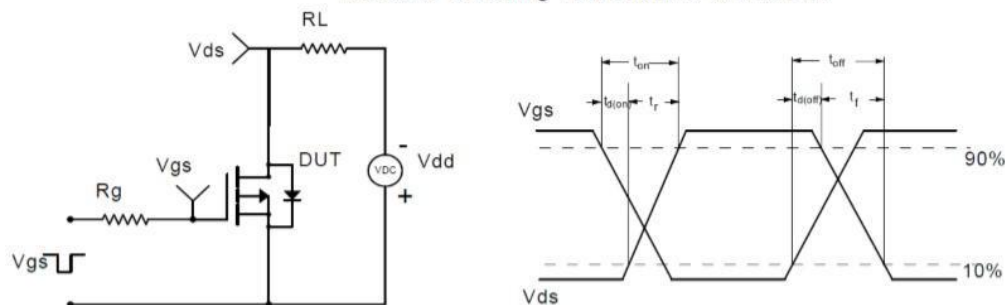


Test Circuit

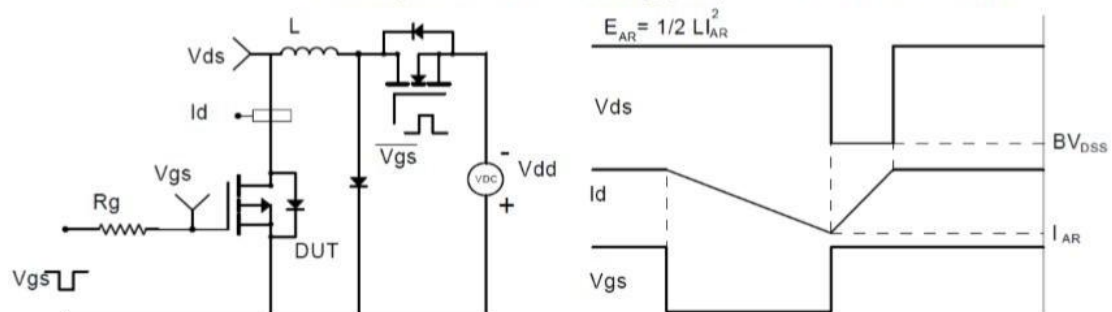
Gate Charge Test Circuit & Waveform



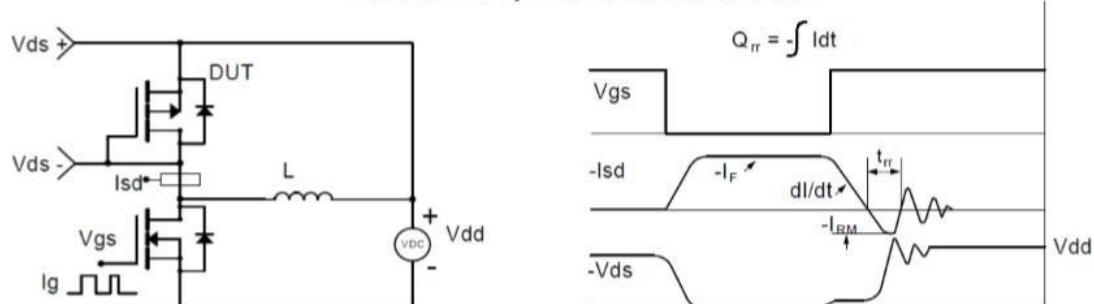
Resistive Switching Test Circuit & Waveforms



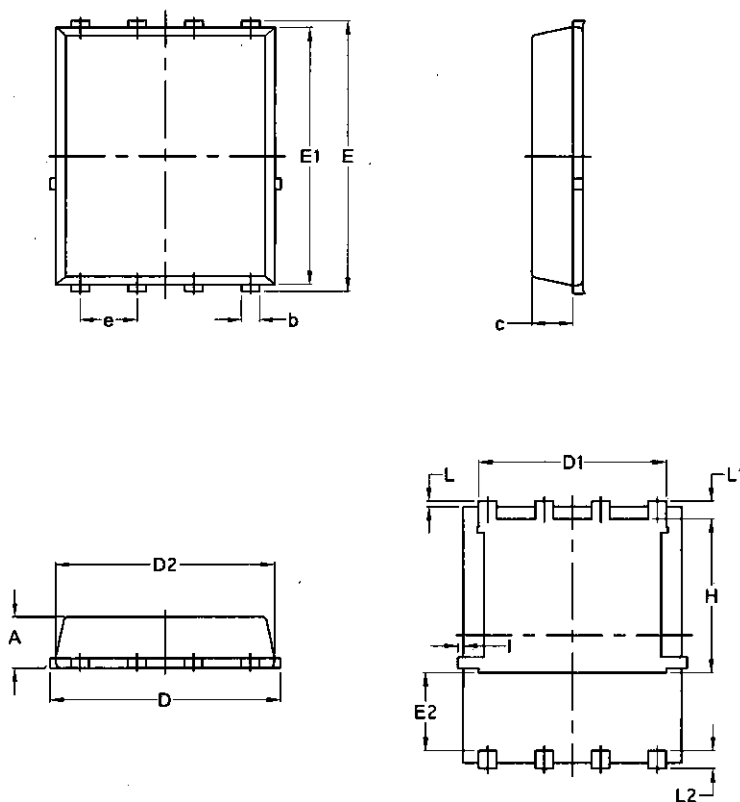
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Mechanical Data-PDFN5060-8L-JQ Single



Symbol	Common			
	mm		Inch	
	Mim	Max	Min	Max
A	1.03	1.17	0.0406	0.0461
b	0.34	0.48	0.0134	0.0189
c	0.824	0.0970	0.0324	0.082
D	4.80	5.40	0.1890	0.2126
D1	4.11	4.31	0.1618	0.1697
D2	4.80	5.00	0.1890	0.1969
E	5.95	6.15	0.2343	0.2421
E1	5.65	5.85	0.2224	0.2303
E2	1.60	/	0.0630	/
e	1.27 BSC		0.05 BSC	
L	0.05	0.25	0.0020	0.0098
L1	0.38	0.50	0.0150	0.0197
L2	0.38	0.50	0.0150	0.0197
H	3.30	3.50	0.1299	0.1378
I	/	0.18	/	0.0070